



501.39082X00

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: S. UNO, et al.  
Application No.: 09/646,671  
Filed: September 20, 2000  
For: METHOD OF MANUFACTURE OF SEMICONDUCTOR  
INTEGRATED CIRCUIT DEVICE AND  
SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE  
(As Amended)  
Group: 2812  
Examiner: L. Gurley

**AMENDMENT**

Mail Stop Amendment  
Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

October 21, 2004

Sir:

In response to the Office Action mailed May 21, 2004, please amend the above-identified application as listed in the following, and as set forth on the following pages:

Amendments to the Claims; and

Remarks are included following the amendments.